

WARP 2008  
June 21-22, 2008

# CALL FOR PAPERS

## The 3<sup>rd</sup> Workshop on Architectural Research Prototyping

Held in conjunction with ISCA-35

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### Organizing Committee

David Penry, BYU  
Shih-Lien Lu, Intel  
Dong Liu, Intel

### Program Committee

Arvind, MIT  
Krste Asanović, UC Berkeley  
Dong Liu, Intel  
Shih-Lien Lu, Intel  
David Penry, BYU

WARP is a forum for the builders in our community to share their practical on-the-ground experiences, to provide a status update on their progress, and to convey insights for those considering prototyping, emulating, or modeling their ideas. This year's workshop will be held in two half-day sessions.

Talks that address the following broad areas are encouraged:

- Hardware prototyping
- Emulation
- Performance modeling using reconfigurable hardware
- Integration of prototyping, modeling, and simulation methodologies

Subjects within these areas might include:

- Status updates on current prototyping, emulation, and performance modeling efforts
- Announcements and descriptions of infrastructure being made available to the community
- Practical guidance on what works & what doesn't, including strategies for:
  - Specification
  - Implementation
  - Verification and validation
  - Finding funding
  - Balancing students' thesis goals versus prototype engineering work
  - Balancing the often conflicting goals of prototypes being research vehicles and high-performance machines.
- Trade-offs in implementation technologies (e.g., FPGA, ASIC, full-custom)

Contributions are encouraged from all members of the architecture community, including those considering embarking on a prototyping, emulation, or modeling effort. The organizers of the workshop will be in contact with the submission authors to follow up on what sort of content they would be interested in presenting. Our goal is to have a lively forum with plenty of scope for participant interaction.

### **Submission Details**

Participants are invited to submit a PDF extended abstract of up to two pages (no denser than double column, 10 point, single-spaced). You may include an appendix of any length but we will not promise to read it. The purpose of this appendix is to include things such as pictures of your prototype, tables, figures, or other items that help clarify the two page abstract. Demonstration proposals are welcome, either standalone or in conjunction with a talk abstract, and should be a maximum of two pages. Submissions exceeding guideline lengths will be summarily rejected. Extensions of the deadline will not be granted. Early submissions will be happily accepted.

- Email submissions (PDF format only) to "warp2008 at ee dot byu dot edu" by April 30, 2008. The attached PDF files should be titled with first author's surname and institution (e.g., smith-ucxy.pdf).
- Author notification by May 16, 2008.

Abstracts and slide presentations will be published online following the workshop.